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## **GLC-SX-MM**



#### **Compliance:**

- IEEE 802.3z Gigabit Ethernet
- SFP MSA SFF-8472
- RoHS
- Class 1 laser product EN 60825

#### Applications

• 1.25GBd Gigabit Ethernet

The GLC-SX-MM is programmed to be fully compatible and functional with all intended CISCO 1GB switching devices. This SFP optical transceiver is based on the Gigabit Ethernet IEEE 802.3 standard and is designed to be compliant with SFF-8472 SFP Multi-source Agreement (MSA). This module is designed for multimode fiber and operates at a nominal wavelength of 850nm.

#### Features:

- Up to 1.25GBd bi-directional data links
- Hot-pluggable SFP footprint
- 850nm VCSEL laser transmitter
- Duplex LC Connectors
- Up to 300M over 62.5/125 MMF
- Up to 550M over 50/125 MMF
- Single power supply 3.3V
- Operating temperature range C-Temp: 0° to 70°

#### **General Specifications**

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Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Data Rate	DR		1.25		GBd	IEEE 802.3
Bit Error Rate	BER			10-12		
Input Voltage	V <sub>cc</sub>	3	3.3	3.6	V	
Maximum Voltage	V <sub>MAX</sub>	-0.5		4	V	Electric Power Interface
Supply Current	I <sub>s</sub>		180	240	mA	Electric Power Interface
Storage Temperature	T <sub>sto</sub>	-40		85	°C	Ambient Temperature



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#### **Block Diagram of Transceiver**



#### Transmitter Section

The Laser driver accept differential input data and provide bias and modulation currents for driving a laser. An automatic power-control (APC) feedback loop is incorporated to maintain a constant average optical power.1310 DFB in an eye safe optical subassembly (OSA) mates to the fiber cable.

**TX\_DISABLE**- The TX\_DISABLE signal is high (TTL logic "1") to turn off the laser output. The laser will turn on within 1ms when TX\_DISABLE is low (TTL logic "0").

**TX\_FAULT**- When the TX\_FAULT signal is high, output indicates a laser fault of some kind. Low indicates normal operation.

**Receiver Section-** The receiver utilizes a PIN detector integrated with a trans-impedance preamplifier in an OSA. This OSA is connected to a Limiting Amplifier which providing post-amplification quantization, and optical signal detection. The limiting Amplifier is AC-coupled to the transimpedance amplifier, with internal  $100\Omega$  differential termination.

**Receive Loss (RX\_LOS)**- The RX\_LOS is high (logic "1") when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in TTL level.

**Controller Section-** The micro controller unit monitors the operation information of LD driver and Limiting Amplifier and report these status to the customer.



Dimensions







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#### **Electrical Pad Layout**





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PIN #	Symbol	Description	Remarks		
1	VEET	Transmitter ground (common with receiver ground)	Circuit ground is isolated from chassis ground		
2	TFAULT	Transmitter Fault			
3	TDIS	Transmitter Disable. Laser output disable on high or open	Disabled: TDIS>2V or open Enabled: TDIS<0.8V		
4	SDA	Data line for serial ID			
5	SCL	Clock line for serial ID	Should Be pulled up with 4.7k - 10k ohm on host board to a voltage between 2V and 3.6V		
6	MOD_ABS	Module Absent. Ground within the module			
7	RS0	No Connection required			
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation	LOS is open collector output		
9	RS1	+3.3V Power Supply			
10	VEER	Receiver ground (common with transmitter ground)	Circuit ground is isolated from chassis ground		
11	VEER	Receiver ground (common with transmitter ground)	-		
12	RD-	Receiver Inverted DATA out. AC coupled			
13	RD+	Receiver Non-inverted DATA out. AC coupled			
14	VEER	Receiver ground (common with transmitter ground)	Circuit ground is isolated from chassis ground		
15	VCCR	Receiver power supply			
16	VCCT	Transmitter power supply	Same as Pin# 1		
17	VEET	Transmitter ground (common with receiver ground)	Circuit ground is connected to chassis ground		
18	TD+	Transmitter Non-inverted DATA out. AC coupled			
19	TD-	Transmitter Inverted DATA out. AC coupled			
20	VEET	Transmitter ground (common with receiver ground)	Circuit ground is connected to chassis ground		

#### Pin Assignment - Pin 1 to Pin 35:

#### References

1. IEEE standard 802.3. IEEE Standard Department, 2002.

2. Small Form Factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 2000.

3. Digital Diagnostics Monitoring Interface for Optical Transceivers -- SFF-8472.